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Performance-based reset of data compression dictionary - US Patent ...

Timer 13 and **up/down counter** 15 constitute a **performance monitor**, the output of ... In **addition**, a reset occurs via line 27 without regard to dictionary ...

www.patentstorm.us/patents/4847619-description.html - 30k - [Cached](#) - [Similar pages](#)

Coverage decoder circuit for **performance counter** - US Patent 7275191

6, 2003, entitled **INCREMENT/DECREMENT CIRCUIT FOR PERFORMANCE COUNTER**;

..... An **adder** 324 adds the 8-bit signal stored in the register 322 to the 48-bit sum ...

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Coverage decoder circuit for **performance counter** - Patent 7275191

6, 2003, entitled **INCREMENT/DECREMENT CIRCUIT FOR PERFORMANCE COUNTER**;

..... The new sum output from the **adder** 324 is input to a MUX circuit 326

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Control apparatus - Patent 4814969

The pulses are supplied to an internal **up/down counter** (not shown) and the count ... In

addition, the fan 40 supplies air to the respective parts of the ...

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Bottom-up approach in automated embedded memory model generation ...

Adding to the challenge of memory modelling is a complication that arises from the

management unit, a time base **up/down counter** and so on. ...

[ieeexplore.ieee.org/iel5/2192/35595/01687680.pdf?](http://ieeexplore.ieee.org/iel5/2192/35595/01687680.pdf?isnumber=35595&prod=&arnumber=1687680&arSt=+...)

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[PDF] RSFQ TECHNOLOGY: CIRCUITS AND SYSTEMS DARREN K. BROCK HYPRES, Inc ...

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synchronous destructive readout (T1-cell), full-**adder**, or even an asynchronous. (reversible)

up-down counter. This template also serves as the basis for the ...

www.hypres.com/papers/Brock-RSFQ-CirSys-Rev01.pdf - [Similar pages](#)

[PDF] Innovate Nordic is a multi-discipline engineering design contest ...

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The waveform can easily be generated with a **up/down counter**, but to avoid **Adder**

and Squarer The **adder** computes a bitwise XOR of two m-bit operands, ...

altera.com/literature/dc/2007/in_2007_nordic.pdf - [Similar pages](#)

(WO/1980/000758) MODULAR PROGRAMMABLE SIGNAL PROCESSOR

The 16-bit work store address counter 124, which is an **up/down counter**, the arithmetic

elem controller 10 executes a **performance monitor** (PM) progr to ...

[www.wipo.int/pctdb/en/wo.jsp?WO=1980%2F00758&IA=WO1980%](http://www.wipo.int/pctdb/en/wo.jsp?WO=1980%2F00758&IA=WO1980%2F00758&DISPLAY=DESC)

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a nedir, a anlamı, definition of a

ardışık toplama, seri toplama, serial **addition**. ardışık toplayıcı, seri toplayıcı, serial **adder**.

ardışık yaklaşıklama, successive approximation ...

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[doc] [Annexure I \(m\) Experiments](#)

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1 **Addition** of two complex numbers. 2 Subtraction of two complex numbers Use of the **performance monitor**. Management of the IIS and FTP server. ...

www.mrei.ac.in/.../ANNEXURE%20I-(m)/Annexure%20I(m)%20%20Experiments/IT-%20List%20of%20Experiments.doc - [Similar pages](#)

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